

	REVISIONS	
ECN	DESCRIPTION	APVD
17442E	UPDATED CUR. IN TOLERANCE	JC
18500E	Updated hardware rev on label to B	JC



	REVISIONS							
REV	DATE	ECN	DESCRIPTION	APVD				
С	08/25/23	17442E	UPDATED CUR. IN TOLERANCE	JC				
D	03/18/25	18500E	Updated hardware rev on label to B	JC				

# **Overview: Module Block Diagram**

DO NOT SCALE DRAWING	į
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THREE PLACE

DECIMAL

+/- 0.020

ANGLES

+/-

CHECKED	J. COOPER	DATE 04/01/2025
APPROVED	J. COOPER	DATE 04/01/2025



## **Overview: Input/Output Configurations**

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REV	DATE	ECN	DESCRIPTION	APVD			
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## Input/Output Configuration Options

Input Type	#1	#2	#3	#4	#5	#6	#7	#8
Digital Active High - Thres	•	•	•	•	٠	•	•	٠
Digital Active Low - Thres	•	•	•	•	•	•	•	٠
Analog 0-36V	•	•	•	•	•	•	•	•
Frequency	•	•	•	•				
PWM Duty Cycle	•	•	•	•				
Resistance	•	•	•	•				
Current (0-20mA)	•	•	•	•				
Quadrature – X1	•*	•*	•*	•*	•*	• <b>*</b>	• <b>*</b>	
Quadrature – X2	•*	•*	•*	•*	•	• •	• <b>*</b>	
Quadrature – X4	•*	•*	•*	•*	•*	•*	•*	
Quadrature – B		•	•	•	•	٠	•	•

Output Type	#1	#2	#3	#4	#5	#6	#7	#8
00 - Disabled	•	٠	•	•	٠	•	•	•
01 – Active HI Digital	•	•	•	•	•	•	•	•
02 – Active LO Digital	•	•	•	•	•	•	•	•
03 – HS PWM Closed Loop	•	•	•	•	•	•	•	•
04 – HS PWM Open Loop	•	•	•	•	•	•	•	•
03 – LS PWM Closed Loop	•	•	•	•	•	•	•	•
04 – LS PWM Open Loop	•	•	•	•	•	•	•	•

In/Out Tolerances (@25C)					
Analog Input	+/-50mV				
Resistance Input	+/-2%				
Current Input (0-20mA)	+/-100uA				
Current Control Output	+/-50mA				

DATE 04/01/2025

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UNLESS OTHE ARE IN INC	erwise specified, e Ches and toleran	DIMENSIONS NCES ARE
		ANGLES
+/- 0.05	+/- 0.020	+/-

DO NOT SCALE DRAWING

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\*When configured as Quadrature, The next adjacent input is automatically configured as the channel B input. (i.e. If In1 = QuadX\_, then In2 = Quad Channel B)

